

Notice of References Cited	Application/Control No. 09/985,676	Applicant(s)/Patent Under Reexamination PATEL ET AL.	
	Examiner Shawki S. Ismail	Art Unit 2155	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-7,145,913	12-2006	Craig et al.	370/401
*	B	US-2002/0176358	11-2002	Assa et al.	370/229
*	C	US-5,793,747	08-1998	Kline, Richard	370/230
*	D	US-2003/0061466	03-2003	Samra et al.	712/217
*	E	US-6,452,933	09-2002	Duffield et al.	370/415
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Thread Prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors" Stuart Fiske and William J. Dally January 1995.
	V	
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.